

Electronic Acknowledgement Receipt

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| EFS ID: | 7927186 |
| Application Number: | 10009649 |
| International Application Number: | |
| Confirmation Number: | 8424 |
| Title of Invention: | METHOD FOR INTERLEAVING A PROGRAM OVER A PLURALITY OF CELLS |
| First Named Inventor/Applicant Name: | Martin Vorbach |
| Customer Number: | 26646 |
| Filer: | Aaron Grunberger/Eunice Chang |
| Filer Authorized By: | Aaron Grunberger |
| Attorney Docket Number: | 2885/56 |
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| Application Type: | U.S. National Stage under 35 USC 371 |

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| Document Number | Document Description | File Name | File Size(Bytes)/ Message Digest | Multi Part /.zip | Pages (if appl.) |
|---|--|--|--|------------------|------------------|
| 1 | Information Disclosure Statement (IDS) Filed (SB/08) | 2885-56-SupplIDS.pdf | 501941 | no | 4 |
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| | | | b25df131f7bd816f6ddebfbabcbf2835ac32b5d7 | | |
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| 3 | Foreign Reference | JP2001-510650.pdf | 82473 | no | 1 |
| | | | ea6beb940da41a08e5aba48dc86601668a014c78 | | |
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| 4 | Foreign Reference | JP2002-33457.pdf | 3129738 | no | 21 |
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| 5 | NPL Documents | altera-trimatrix.pdf | 2519004 | no | 28 |
| | | | 282e58ffc20968d60153f8f74971fc96dd9227a8 | | |
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| 6 | NPL Documents | altera-apex-ii.pdf | 10190010 | no | 99 |
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| 7 | NPL Documents | Becker- Jurgen_PartitioningCompiler. pdf | 20937818 | no | 326 |
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| 8 | NPL Documents | BlueGeneHardwareArchitecture.pdf | 3233814 | no | 23 |
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| 9 | NPL Documents | BlueGeneLNextGen2002.pdf | 7783513 | no | 29 |
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| 10 | NPL Documents | blue-gene-slides.pdf | 4450664 | no | 20 |
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| 11 | NPL Documents | blue-gene-overview.pdf | 3003233 | no | 22 |
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| 12 | NPL Documents | Epstein-IBMExtendsDSP.pdf | 989670 | no | 4 |
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| 13 | NPL Documents | GalanisMD.pdf | 278793 | no | 3 |
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| 14 | NPL Documents | GuoZhi.pdf | 421235 | no | 5 |
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| 15 | NPL Documents | Gwennap-P6UnderscoresIntel.pdf | 3042484 | no | 11 |
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| 16 | NPL Documents | Gwennap-IntelsP6Designed.pdf | 1669036 | no | 7 |
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| 17 | NPL Documents | Hartenstein-ParallelizingCompilation.pdf | 2153639 | no | 13 |
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| 18 | NPL Documents | Hartenstein-Two-Level-Co-Design.pdf | 883370 | no | 10 |
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| 19 | NPL Documents | Huang-et-al-MultiPrec-Floating-Point.pdf | 1070963 | no | 8 |
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| 22 | NPL Documents | Intel-3.pdf | 13688822 | no | 120 |
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| 23 | NPL Documents | Intel-4.pdf | 13193473 | no | 118 |
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| 24 | NPL Documents | Jo-et-al-Floating-PointDesign.pdf | 602920 | no | 4 |
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| 25 | NPL Documents | xilinx-intelligent-clock.pdf | 546831 | no | 5 |
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| 26 | NPL Documents | xilinx-power-consump.pdf | 2630588 59a7ea16f1594a86f07fa7fe20f4950340a66cc9 | no | 21 |
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| 27 | Fee Worksheet (PTO-875) | fee-info.pdf | 30296 bce4168bb8ca28de20156dc4f6e4715a809766f8 | no | 2 |
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